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filed 9/19/06

PTO/SB/08a (08-03)

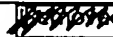
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STATEMENT BY APPLICANT**
(Not for submission under 37 CFR 1.99)

Application Number	10816435
Filing Date	2004-03-31
First Named Inventor	G.Y. TSAO et al.
Art Unit	2111
Examiner Name	Gopal C. Ray
Attorney Docket Number	P19002

U.S. PATENTS


Examiner Initial*	Cite No	Patent Number	Kind Code ¹	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
ECR	1	6760783		2004-07-06	Berry	
ECR	2	7010633		2006-03-07	Arndt et al.	

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U.S. PATENT APPLICATION PUBLICATIONS


Examiner Initial*	Cite No	Publication Number	Kind Code ¹	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
ECR	1	20010037397		2001-11-01	Boucher et al.	
ECR	2	20020152327		2002-10-17	Kagan et al.	
ECR	3	20030065856		2003-04-03	Kagan et al.	
ECR	4	20040017819		2004-01-29	Kagan et al.	

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
GCR	5	20040027374		2004-02-12	Cime et al.	
GCR	6	20040103225		2004-05-27	McAlpine et al.	
GCR	7	20050228922		2005-10-13	Tsao et al.	
GCR	8	20060004983		2006-01-05	Tsao et al.	
GCR	9	20060133396		2006-06-22	Shah et al.	
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	1	BUONADONNA, P., and D. CULLER, "Queue Pair IP: A Hybrid Architecture for System Area Networks", Proceedings of the 29th Annual International Symposium on Computer Architecture, 2002, pp. 247-256.	<input type="checkbox"/>
	2	DEYRING, K. (Ed.), "Serial ATA: High Speed Serialized AT Attachment", Rev. 1.0, August 29, 2001, 36 pp.	<input type="checkbox"/>
	3	INFINIBAND, "InfiniBand Architecture Specification Volume 1", Release 1.0, October 24, 2000, Ch. 1-8, pp. 1-195.	<input type="checkbox"/>
	4	INFINIBAND, "InfiniBand Architecture Specification Volume 1", Release 1.0, October 24, 2000, Ch. 9-10, pp. 196-445.	<input type="checkbox"/>
	5	INFINIBAND, "InfiniBand Architecture Specification Volume 1", Release 1.0, October 24, 2000, Ch. 11-14, pp. 446-669.	<input type="checkbox"/>
	6	INFINIBAND, "InfiniBand Architecture Specification Volume 1", Release 1.0, October 24, 2000, Ch. 15-20, pp. 670-880.	<input type="checkbox"/>
	7	INFINIBAND, "InfiniBand Architecture Specification Volume 2", Release 1.0, October 24, 2000, Ch. 1-11, pp. 1-319.	<input type="checkbox"/>
	8	INFINIBAND, "InfiniBand Architecture Specification Volume 2", Release 1.0, October 24, 2000, Ch. 12-Appendix D, pp. 320-623.	<input type="checkbox"/>
	9	EP Office Action, February 9, 2006, for International Application No. 03 812 441.8-22111.	<input type="checkbox"/>
	10	PCT International Search Report, August 9, 2004, for International Application No. PCT/US03/37254.	<input type="checkbox"/>
	11	PENOKIE, G.O. (Ed.), "Information Technology- SCSI Controller Commands-2 (SCC-2)", T10/Project 1225D, Revision 4, September 12, 1997, 24 pp.	<input type="checkbox"/>

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12	RANGARAJAN, M., A. BOHRA, K. BANERJEE, E.V. CARRERA, and R. BIANCHINI, "TCP Servers: Offloading TCP Processing in Internet Servers. Design, Implementation, and Performance", Technical Report, Rutgers University, 2002, pp. 1-14.	<input type="checkbox"/>
13	RDMA CONSORTIUM, "Architectural Specifications for RDMA over TCP/IP", [online], 2005, [retrieved on December 22, 2005], retrieved from the Internet at <URL: http://www.rdmaconsortium.org/home>.	<input type="checkbox"/>
14	STEVENS, R.W., "UNIX Network Programming", 1990, pp. 209-210.	<input type="checkbox"/>
15	US Final Office Action, May 24, 2006, for US Application No. 10/815,902.	<input type="checkbox"/>
16	US First Office Action, December 13, 2005, for US Application No. 10/815,902.	<input type="checkbox"/>
17	"Virtual Interface Architecture Specification", Draft Revision 1.0, December 4, 1997, 83 pp.	<input type="checkbox"/>

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Examiner Signature	Gopal C. Ray	Date Considered	10/6/06
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